Harel St. 11, Haifa 3455520, Israel
Tel: (972) 54-3339353
E-mail: itsimer@gmail.com

Igor Tsimerman

Objective:

To secure people & technical leadership position in IT industry where my skills will be put to use.

Experience:

RealSense - Intel Inc. Israel – Haifa   **(Jun'2019 - present**)
System Integration Manager
- Leading system engineering and integration team in RealSense 3D camera department at Intel (10-12 people)
- Main team focuses:
    o Product features definition, including validation plan
    o Leading system release integration and HW bring-up activities with other R&D teams like: mechanics, optics, algo, HW and FW
    o Support of all related R&D teams. Including: POC & characterization experiments, tester definition, tester bring-up and product calibration
    o Design verification & HVM failure analysis

Intel Inc. Israel – Haifa   **(Sep'2014– May'2019**)
Program Architect
- Program architect at pre/post silicon validation IP development group
- Responsible for technical aspects and strategic decisions for validation IP roadmap
- Leading TRs & path findings of new projects by interfacing customers, development teams and external vendors

Intel Inc. Israel – Haifa   **(Jun'2012 – Sep'2014)**
Technical Project Manager
- Led full life cycle development of Intel's validation/debug/prototyping systems for Intel's internal customers in the SoC Pre/Post-Silicon verification groups
- Was responsible for all project phases: requirements, definition, planning, execution, deployment and support
- Participated in system architecture definition & ratification
- Led project stakeholders in matrix environment (board design, mechanical design, FPGA design, low-level SW design, logistics and purchasing)
- Acted as the owner of group's project management methodology

Intel Inc. Israel – Haifa   **(Sep'2009 – 2012**)
"Board design" team manager
- Led a team of five board designers
- Was responsible for people management and team's development
- Performed estimation of effort, schedule and cost analysis
- Participated in project high level architecture definition

Intel Inc. Israel – Haifa   **(2005 – 2009)**
HW engineer – board designer
- Defined board architecture
- Designed high-speed digital and analog systems
- Led system layout and design-to-manufacturing activities
- Experienced in stand-alone and team player design projects

Intel Inc. Israel – Haifa   **(2004 – 2005)**
Student/Intern position
- Participated in board design activities
- Prepared technical documentation
- Debugged systems and prepared lab experiences

Education:

Master of business Administration (MBA)   **(2008 - 2010)**
Technion Israel Institute of Technology, Haifa
Average grade: 92/100

Bachelor of Science (B.Sc.) , Electronic Engineering
Technion Israel Institute of Technology, Haifa
Fields of specialization: Micro&Nano technology; Biological signals   **(2001 - 2005**)
Average grade: 84/100 (Cum Laude)

Professional courses:

Sapir-CS – Project Management Professional (36 hours)
Intel – Various people & project management courses
Hi-tech college – High-speed digital design **(**2 weeks**)**

Publications:

Shani Meir, Oded David, Adams Guy, Tsimerman Igor, "Tracing IP Prototyping— Concept to Solution", Intel's Design and Test Technology Conference, 2013

Languages:

- Hebrew (mother tongue level)
- Russian (mother tongue level)
- English (fluently)

Technical & professional skills:

- Project management
- Tasks and people lead
- System definition & architecture
- Collaborative work with customers, multiple development teams & external vendors.
- Top to bottom PCB development

REFERENCES AVAILABLE UPON REQUEST